

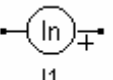
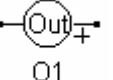
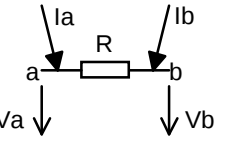
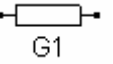
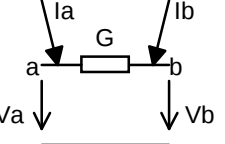
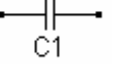
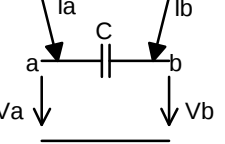
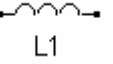
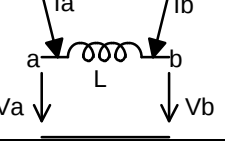
Prvky z knihovny programu SNAP 2.6, aktualizace z 1.7.2004

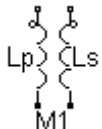
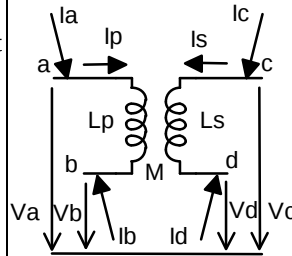
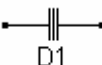
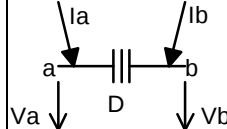
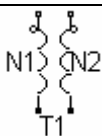
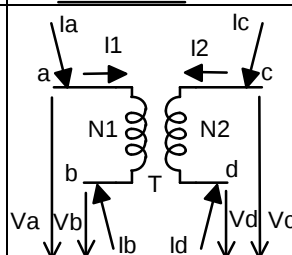
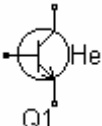
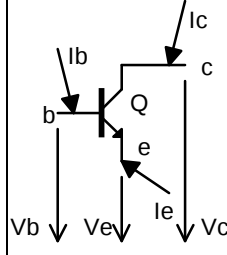
Schématické značky jsou definovány v souboru **SNAP.LIB**.

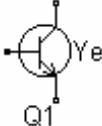
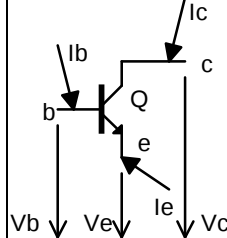
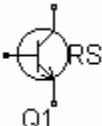
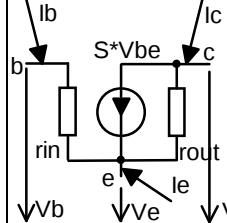
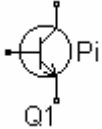
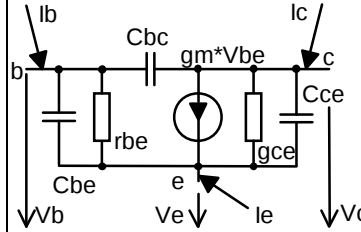
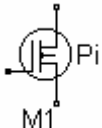
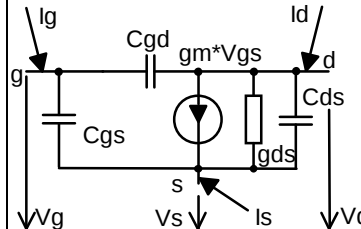
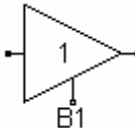
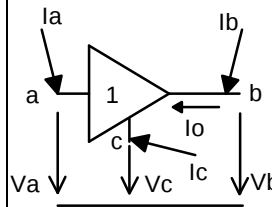
Matematické modely jsou definovány v souboru **SNAP.CDL**.

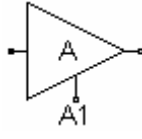
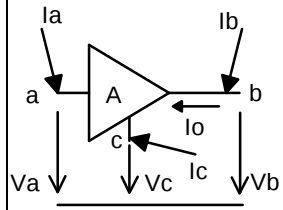
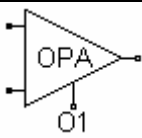
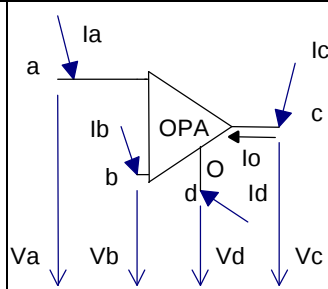
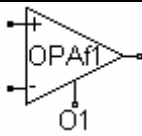
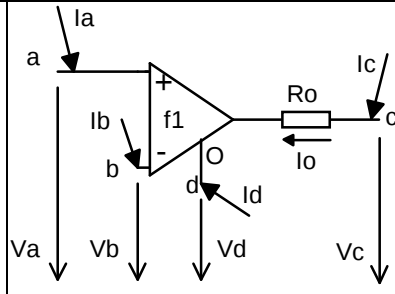
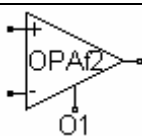
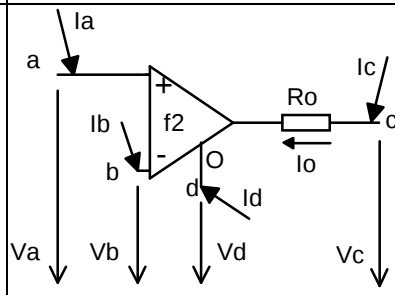
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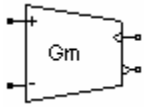
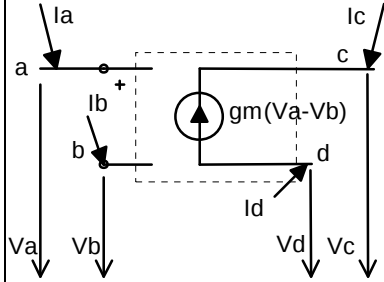
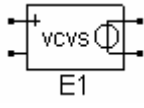
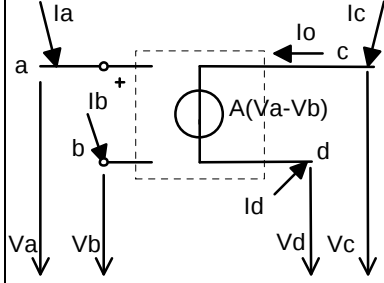
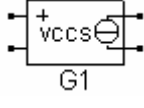
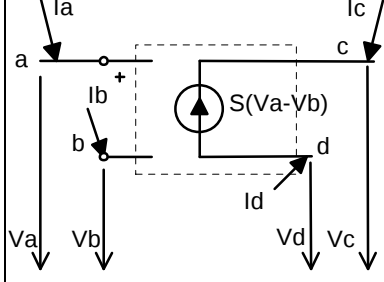
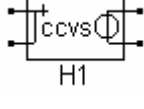
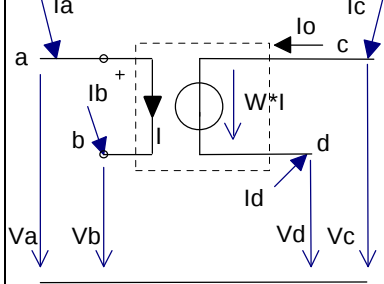
$$\begin{bmatrix} I_a \\ I_b \\ I_c \\ I_d \\ 0 \end{bmatrix} = \begin{bmatrix} a & b & c & d & e \\ f & g & h & i & j \\ k & l & m & n & o \\ p & q & r & s & t \\ u & v & w & x & y \end{bmatrix} \cdot \begin{bmatrix} V_a \\ V_b \\ V_c \\ V_d \\ I_2 \end{bmatrix} \Rightarrow \begin{bmatrix} V_a & V_b & V_c & V_d & I_2 \\ I_a & a & b & c & d & e \\ I_b & f & g & h & i & j \\ I_c & k & l & m & n & o \\ I_d & p & q & r & s & t \\ & u & v & w & x & y \end{bmatrix}$$

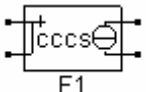
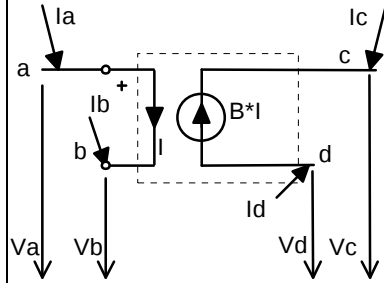
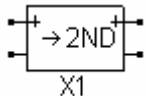
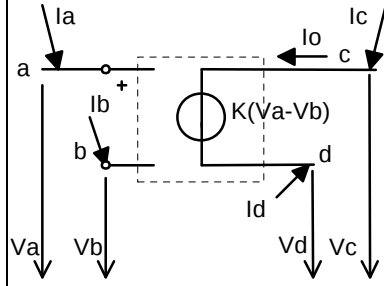
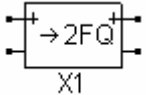
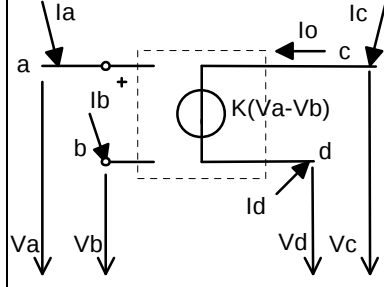
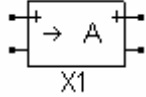
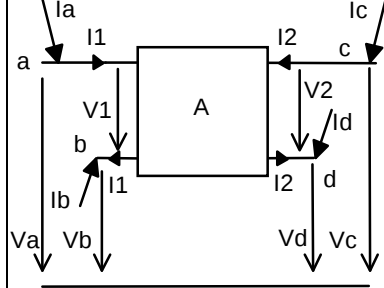
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vstup	input	 I1	-		
výstup	output	 O1	-		
rezistor	R	 R1	R - odpor		$\begin{bmatrix} V_a & V_b \\ I_a & 1/R & -1/R \\ I_b & -1/R & 1/R \end{bmatrix}$
konduktor	G	 G1	G - vodivost		$\begin{bmatrix} V_a & V_b \\ I_a & G & -G \\ I_b & -G & G \end{bmatrix}$
kapacitor	C	 C1	C - kapacita		$\begin{bmatrix} V_a & V_b \\ I_a & sC & -sC \\ I_b & -sC & sC \end{bmatrix}$
induktor	L	 L1	L - indukčnost		$\begin{bmatrix} V_a & V_b \\ I_a & 1/sL & -1/sL \\ I_b & -1/sL & 1/sL \end{bmatrix}$

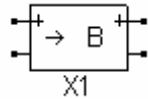
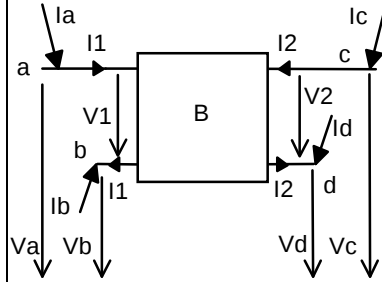
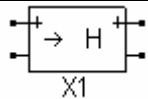
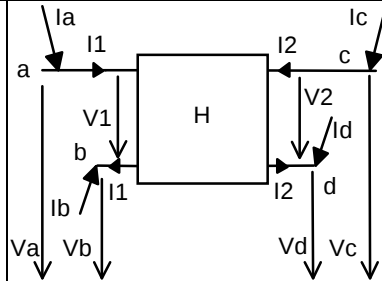
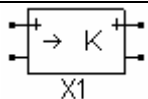
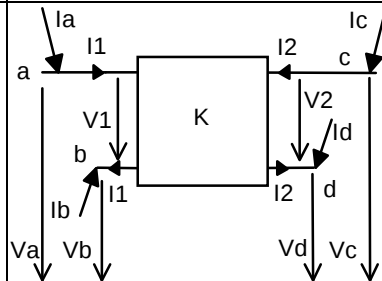
obvod s vzájemnou indukčností	M		L_p – primární indukčnost L_s – sekundární indukčnost M – vzájemná indukčnost		<table><tr><th></th><th>V_a</th><th>V_b</th><th>V_c</th><th>V_d</th><th>I_p</th><th>I_s</th></tr><tr><td>I_a</td><td></td><td></td><td></td><td></td><td>1</td><td></td></tr><tr><td>I_b</td><td></td><td></td><td></td><td></td><td>-1</td><td></td></tr><tr><td>I_c</td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_d</td><td></td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>-1</td><td>1</td><td></td><td></td><td>sL_p</td><td>sM</td></tr><tr><td></td><td></td><td></td><td>-1</td><td>1</td><td>sM</td><td>sL_s</td></tr></table>		V_a	V_b	V_c	V_d	I_p	I_s	I_a					1		I_b					-1		I_c						1	I_d						-1		-1	1			sL_p	sM				-1	1	sM	sL_s
	V_a	V_b	V_c	V_d	I_p	I_s																																																
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			-1	1	sM	sL_s																																																
dvojný kapacitor	$FDNR$		D – dvojná kapacita		<table><tr><th></th><th>V_a</th><th>V_b</th></tr><tr><td>I_a</td><td>$s^2 D$</td><td>$-s^2 D$</td></tr><tr><td>I_b</td><td>$-s^2 D$</td><td>$s^2 D$</td></tr></table>		V_a	V_b	I_a	$s^2 D$	$-s^2 D$	I_b	$-s^2 D$	$s^2 D$																																								
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I_a	$s^2 D$	$-s^2 D$																																																				
I_b	$-s^2 D$	$s^2 D$																																																				
transformátor	<i>Trans- former</i>		n – transformační poměr N_2/N_1		<table><tr><th></th><th>V_a</th><th>V_b</th><th>V_c</th><th>V_d</th><th>I_2</th></tr><tr><td>I_a</td><td></td><td></td><td></td><td></td><td>$-n$</td></tr><tr><td>I_b</td><td></td><td></td><td></td><td></td><td>n</td></tr><tr><td>I_c</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_d</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>n</td><td>$-n$</td><td>-1</td><td>1</td><td></td></tr></table>		V_a	V_b	V_c	V_d	I_2	I_a					$-n$	I_b					n	I_c					1	I_d					-1		n	$-n$	-1	1														
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I_b					n																																																	
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tranzistor s H- parametry	<i>Transistor He</i>		$h_{11e}, h_{12e}, h_{21e}, h_{22e}$ – H-parametry tranzistoru v zapojení se společným emitemem $y_{11} = 1/h_{11}$ $y_{12} = -h_{12}/h_{11}$ $y_{21} = h_{21}/h_{11}$ $y_{22} = h_{22} - h_{12}h_{21}/h_{11}$		<table><tr><th></th><th>V_b</th><th>V_c</th><th>V_e</th></tr><tr><td>I_b</td><td>y_{11}</td><td>y_{12}</td><td>$-y_{11} - y_{12}$</td></tr><tr><td>I_c</td><td>y_{21}</td><td>y_{22}</td><td>$-y_{21} - y_{22}$</td></tr><tr><td>I_e</td><td>$-y_{11} - y_{21}$</td><td>$-y_{12} - y_{22}$</td><td>$y_{11} + y_{12} + y_{21} + y_{22}$</td></tr></table>		V_b	V_c	V_e	I_b	y_{11}	y_{12}	$-y_{11} - y_{12}$	I_c	y_{21}	y_{22}	$-y_{21} - y_{22}$	I_e	$-y_{11} - y_{21}$	$-y_{12} - y_{22}$	$y_{11} + y_{12} + y_{21} + y_{22}$																																	
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I_c	y_{21}	y_{22}	$-y_{21} - y_{22}$																																																			
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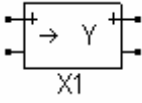
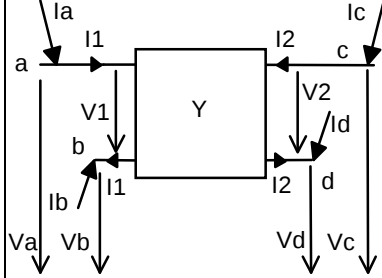
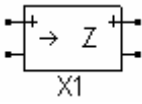
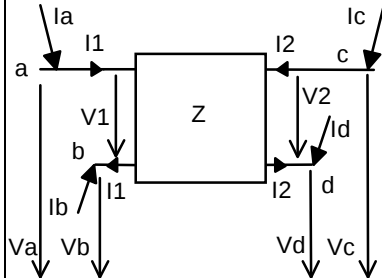
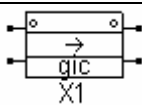
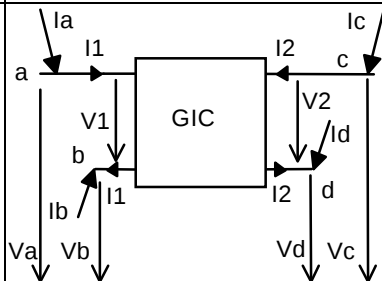
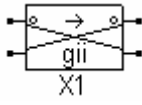
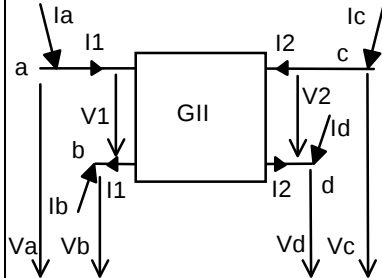
tranzistor s Y-parametry	Transistor_Ye		y11e, y12e, y21e, y22e – Y-parametry tranzistoru v zapojení se společným emitorem		<table><tr><td></td><td>Vb</td><td>Vc</td><td>Ve</td></tr><tr><td>Ib</td><td>y11</td><td>y12</td><td>-y11 - y12</td></tr><tr><td>Ic</td><td>y21</td><td>y22</td><td>-y21 - y22</td></tr><tr><td>Ie</td><td>-y11 - y21</td><td>-y12 - y22</td><td>y11 + y12 + y21 + y22</td></tr></table>		Vb	Vc	Ve	Ib	y11	y12	-y11 - y12	Ic	y21	y22	-y21 - y22	Ie	-y11 - y21	-y12 - y22	y11 + y12 + y21 + y22									
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Ic	y21	y22	-y21 - y22																											
Ie	-y11 - y21	-y12 - y22	y11 + y12 + y21 + y22																											
tranzistor – zjednodušený model pro výpočty v nf pásmu	Transistor_RS		rin – vstupní odpor B-E S – strmost Ic/Uce rout - výstupní odpor C-E		<table><tr><td></td><td>Vb</td><td>Vc</td><td>Ve</td></tr><tr><td>Ib</td><td>1/rin</td><td></td><td>-1/rin</td></tr><tr><td>Ic</td><td>-S</td><td>1/rout</td><td>S - 1/rout</td></tr><tr><td>Ie</td><td>S - 1/rin</td><td>-1/rout</td><td>1/rin + 1/rout - S</td></tr></table>		Vb	Vc	Ve	Ib	1/rin		-1/rin	Ic	-S	1/rout	S - 1/rout	Ie	S - 1/rin	-1/rout	1/rin + 1/rout - S									
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Ic	-S	1/rout	S - 1/rout																											
Ie	S - 1/rin	-1/rout	1/rin + 1/rout - S																											
Π - model bipolárního tranzistoru	Transistor_Pi		rbe - vstupní odpor B-E gm - strmost Ic/Uce gce – výstupní vodivost C-E Cbe – kapacita B-E Cbc – kapacita B-C Cce – kapacita C-E		<table><tr><td></td><td>Vb</td><td>Vc</td><td>Ve</td></tr><tr><td>Ib</td><td>s(Cbe + Cbc) + gbe</td><td>-sCbc</td><td>-gbe - sCbe</td></tr><tr><td>Ic</td><td>-sCbc + gm</td><td>s(Cce + Cbc) + gce</td><td>-gm - gce - sCce</td></tr><tr><td>Ie</td><td>-gbe - gm - sCbe</td><td>-gce - sCce</td><td>gm + gce + gbe + s(Cce + Cbe)</td></tr></table>		Vb	Vc	Ve	Ib	s(Cbe + Cbc) + gbe	-sCbc	-gbe - sCbe	Ic	-sCbc + gm	s(Cce + Cbc) + gce	-gm - gce - sCce	Ie	-gbe - gm - sCbe	-gce - sCce	gm + gce + gbe + s(Cce + Cbe)									
	Vb	Vc	Ve																											
Ib	s(Cbe + Cbc) + gbe	-sCbc	-gbe - sCbe																											
Ic	-sCbc + gm	s(Cce + Cbc) + gce	-gm - gce - sCce																											
Ie	-gbe - gm - sCbe	-gce - sCce	gm + gce + gbe + s(Cce + Cbe)																											
Π - model unipolárního tranzistoru	Fet_Pi		gm - strmost Id/Uds gds – vstupní vodivost D-S Cgs – kapacita G-S Cgd – kapacita G-D Cds – kapacita D-S		<table><tr><td></td><td>Vg</td><td>Vd</td><td>Vs</td></tr><tr><td>Ig</td><td>s(Cgs + Cgd)</td><td>-sCgd</td><td>-sCgs</td></tr><tr><td>Id</td><td>-sCgd + gm</td><td>s(Cds + Cgd) + gds</td><td>-gm - gds - sCds</td></tr><tr><td>Is</td><td>-gm - sCgs</td><td>-gds - sCds</td><td>gm + gds + s(Cds + Cgs)</td></tr></table>		Vg	Vd	Vs	Ig	s(Cgs + Cgd)	-sCgd	-sCgs	Id	-sCgd + gm	s(Cds + Cgd) + gds	-gm - gds - sCds	Is	-gm - sCgs	-gds - sCds	gm + gds + s(Cds + Cgs)									
	Vg	Vd	Vs																											
Ig	s(Cgs + Cgd)	-sCgd	-sCgs																											
Id	-sCgd + gm	s(Cds + Cgd) + gds	-gm - gds - sCds																											
Is	-gm - sCgs	-gds - sCds	gm + gds + s(Cds + Cgs)																											
jednotkový zesilovač	Buffer		-		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>I0</td></tr><tr><td>Ia</td><td></td><td></td><td></td><td></td></tr><tr><td>Ib</td><td></td><td></td><td></td><td>1</td></tr><tr><td>Ic</td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>1</td><td>-1</td><td></td><td></td></tr></table>		Va	Vb	Vc	I0	Ia					Ib				1	Ic				-1		1	-1		
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ideální zesilovač napětí	Ampli		A ..napěťové zesílení		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>I0</td></tr><tr><td>Ia</td><td></td><td></td><td></td><td></td></tr><tr><td>Ib</td><td></td><td></td><td></td><td>1</td></tr><tr><td>Ic</td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>A</td><td>-1</td><td></td><td></td></tr></table>		Va	Vb	Vc	I0	Ia					Ib				1	Ic				-1		A	-1																			
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OZ s 1 kmitočtem lomu	OPA_f1		A – ss zesílení otevřené smyčky GBW =ft – tranzitní kmitočet wt =2p ft Ro – výstupní odpor		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>Vd</td><td>I0</td></tr><tr><td>Ia</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>Ib</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>Ic</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>Id</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>Aw_t wt + sA</td><td>-Aw_t wt + sA</td><td>-1</td><td></td><td>R0</td></tr></table>		Va	Vb	Vc	Vd	I0	Ia						Ib						Ic					1	Id					-1		Aw_t wt + sA	-Aw_t wt + sA	-1		R0						
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OZ s 2 kmitočty lomu	OPA_f2		A – ss zesílení otevřené smyčky GBW =ft – tranzitní kmitočet wt =2p ft f2 – kmitočet 2. lomu w2 =2p f2 Ro – výstupní odpor		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>Vd</td><td>I0</td></tr><tr><td>Ia</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>Ib</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>Ic</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>Id</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>Aw_t wt + sA</td><td>w2 w2 + s</td><td>-Aw_t wt + sA</td><td>w2 w2 + s</td><td>-1</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td>R0</td></tr></table>		Va	Vb	Vc	Vd	I0	Ia						Ib						Ic					1	Id					-1		Aw_t wt + sA	w2 w2 + s	-Aw_t wt + sA	w2 w2 + s	-1						R0
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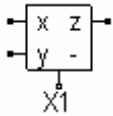
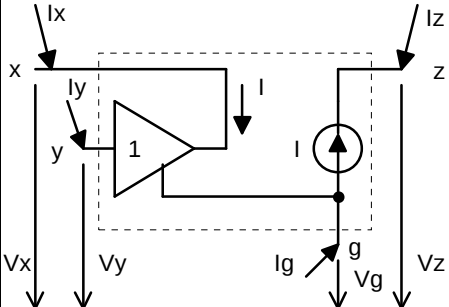
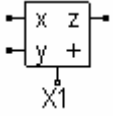
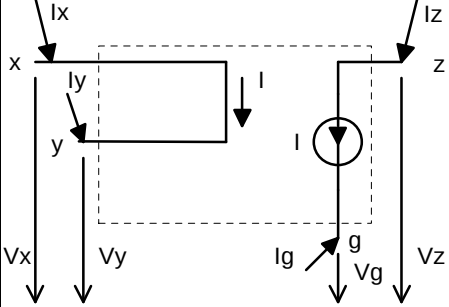
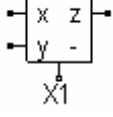
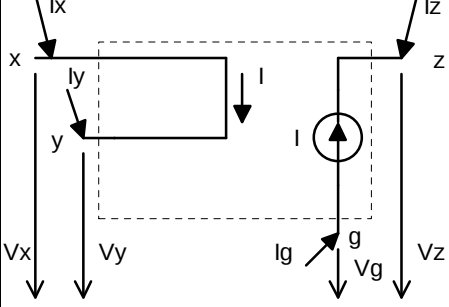
operační transkonduk- tanční zesilovač se 2 výstupy	BOTA		gm - transkonduktance		<table><tr><th></th><th>Va</th><th>Vb</th><th>Vc</th><th>Vd</th></tr><tr><td>Ia</td><td></td><td></td><td></td><td></td></tr><tr><td>Ib</td><td></td><td></td><td></td><td></td></tr><tr><td>Ic</td><td>-gm</td><td>gm</td><td></td><td></td></tr><tr><td>Id</td><td>gm</td><td>-gm</td><td></td><td></td></tr></table>		Va	Vb	Vc	Vd	Ia					Ib					Ic	-gm	gm			Id	gm	-gm																										
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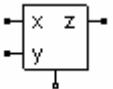
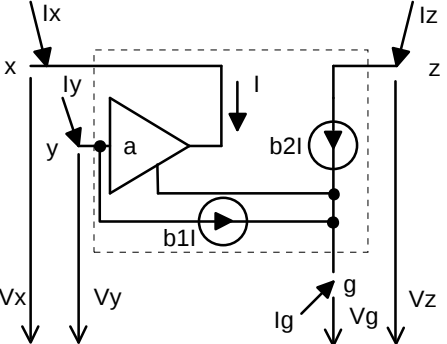
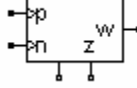
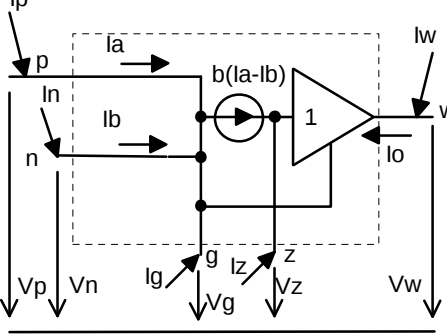
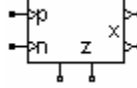
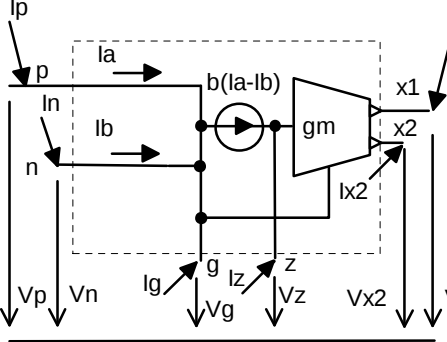
zdroj proudu řízený proudem	CCCS		B – Iout/Iin		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td><td>I</td></tr><tr><td>I_a</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_b</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td>I_c</td><td></td><td></td><td></td><td></td><td>-B</td></tr><tr><td>I_d</td><td></td><td></td><td></td><td></td><td>B</td></tr><tr><td></td><td>1</td><td>-1</td><td></td><td></td><td></td></tr></table>		V _a	V _b	V _c	V _d	I	I _a					1	I _b					-1	I _c					-B	I _d					B		1	-1			
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obvod 2. řádu daný koeficienty	2nd		a ₂ , a ₁ , a ₀ – koeficienty čitatele přenosové funkce b ₂ , b ₁ , b ₀ – koeficienty jmenovatele přenosové funkce $K = \frac{a_2 s^2 + a_1 s + a_0}{b_2 s^2 + b_1 s + b_0}$		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td><td>I₀</td></tr><tr><td>I_a</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>I_b</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>I_c</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_d</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>K</td><td>-K</td><td>-1</td><td>1</td><td></td></tr></table>		V _a	V _b	V _c	V _d	I ₀	I _a						I _b						I _c					1	I _d					-1		K	-K	-1	1	
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obvod 2. řádu daný parametry f ₀ a Q	2fQ		a ₂ , a ₁ , a ₀ – koeficienty čitatele přenosové funkce f ₀ – charakteristický kmitočet, w ₀ = 2pf ₀ Q – činitel jakosti $K = \frac{a_2 s^2 + a_1 s + a_0}{s^2 + s \frac{w_0}{Q} + w_0^2}$		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td><td>I₀</td></tr><tr><td>I_a</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>I_b</td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>I_c</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_d</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>K</td><td>-K</td><td>-1</td><td>1</td><td></td></tr></table>		V _a	V _b	V _c	V _d	I ₀	I _a						I _b						I _c					1	I _d					-1		K	-K	-1	1	
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I _d					-1																																				
	K	-K	-1	1																																					
dvojbran popsaný parametry A	2-port_A		a ₁₁ , a ₁₂ , a ₂₁ , a ₂₂ – přímé kaskádní parametry <table><tr><td>V₁</td><td>a₁₁</td><td>a₁₂</td><td>V₂</td></tr><tr><td>I₁</td><td>a₂₁</td><td>a₂₂</td><td>-I₂</td></tr></table> y ₁₁ = a ₂₂ / a ₁₂ y ₁₂ = a ₂₁ - a ₁₁ a ₂₂ / a ₁₂ y ₂₁ = -1/ a ₁₂ y ₂₂ = a ₁₁ / a ₁₂	V ₁	a ₁₁	a ₁₂	V ₂	I ₁	a ₂₁	a ₂₂	-I ₂		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td></tr><tr><td>I_a</td><td>y₁₁</td><td>-y₁₁</td><td>y₁₂</td><td>-y₁₂</td></tr><tr><td>I_b</td><td>-y₁₁</td><td>y₁₁</td><td>-y₁₂</td><td>y₁₂</td></tr><tr><td>I_c</td><td>y₂₁</td><td>-y₂₁</td><td>y₂₂</td><td>-y₂₂</td></tr><tr><td>I_d</td><td>-y₂₁</td><td>y₂₁</td><td>-y₂₂</td><td>y₂₂</td></tr></table>		V _a	V _b	V _c	V _d	I _a	y ₁₁	-y ₁₁	y ₁₂	-y ₁₂	I _b	-y ₁₁	y ₁₁	-y ₁₂	y ₁₂	I _c	y ₂₁	-y ₂₁	y ₂₂	-y ₂₂	I _d	-y ₂₁	y ₂₁	-y ₂₂	y ₂₂			
V ₁	a ₁₁	a ₁₂	V ₂																																						
I ₁	a ₂₁	a ₂₂	-I ₂																																						
	V _a	V _b	V _c	V _d																																					
I _a	y ₁₁	-y ₁₁	y ₁₂	-y ₁₂																																					
I _b	-y ₁₁	y ₁₁	-y ₁₂	y ₁₂																																					
I _c	y ₂₁	-y ₂₁	y ₂₂	-y ₂₂																																					
I _d	-y ₂₁	y ₂₁	-y ₂₂	y ₂₂																																					

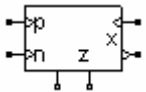
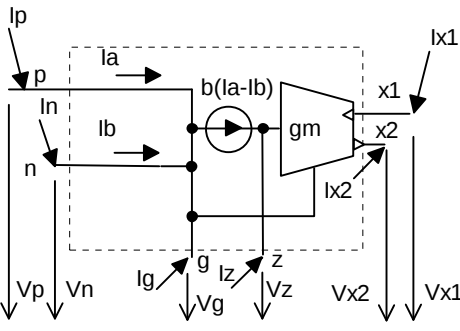
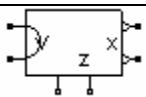
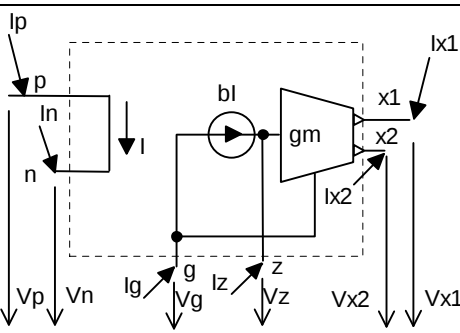
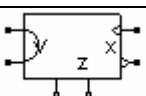
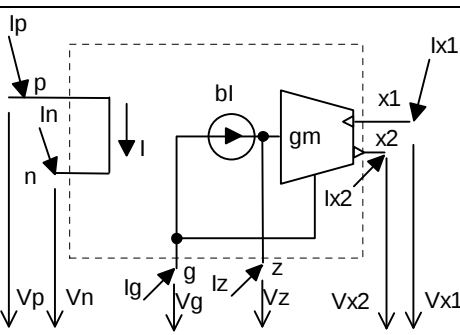
dvojbran popsaný parametry B	2-port_B		b₁₁, b₁₂, b₂₁, b₂₂ – zpětné kaskádní parametry $\begin{bmatrix} V_2 \\ -I_2 \end{bmatrix} = \begin{bmatrix} b_{11} & b_{12} \\ b_{21} & b_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ I_1 \end{bmatrix}$ $y_{11} = -b_{11} / b_{12}$ $y_{12} = -1 / b_{12}$ $y_{21} = b_{11} b_{22} / b_{12} - b_{21}$ $y_{22} = -b_{22} / b_{12}$		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td></tr><tr><td>I_a</td><td>y_{11}</td><td>$-y_{11}$</td><td>y_{12}</td><td>$-y_{12}$</td></tr><tr><td>I_b</td><td>$-y_{11}$</td><td>y_{11}</td><td>$-y_{12}$</td><td>y_{12}</td></tr><tr><td>I_c</td><td>y_{21}</td><td>$-y_{21}$</td><td>y_{22}</td><td>$-y_{22}$</td></tr><tr><td>I_d</td><td>$-y_{21}$</td><td>y_{21}</td><td>$-y_{22}$</td><td>y_{22}</td></tr></table>		V_a	V_b	V_c	V_d	I_a	y_{11}	$-y_{11}$	y_{12}	$-y_{12}$	I_b	$-y_{11}$	y_{11}	$-y_{12}$	y_{12}	I_c	y_{21}	$-y_{21}$	y_{22}	$-y_{22}$	I_d	$-y_{21}$	y_{21}	$-y_{22}$	y_{22}
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I_a	y_{11}	$-y_{11}$	y_{12}	$-y_{12}$																										
I_b	$-y_{11}$	y_{11}	$-y_{12}$	y_{12}																										
I_c	y_{21}	$-y_{21}$	y_{22}	$-y_{22}$																										
I_d	$-y_{21}$	y_{21}	$-y_{22}$	y_{22}																										
dvojbran popsaný parametry H	2-port_H		h₁₁, h₁₂, h₂₁, h₂₂ – paralelně-sériové hybridní parametry $\begin{bmatrix} V_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} h_{11} & h_{12} \\ h_{21} & h_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ V_2 \end{bmatrix}$ $y_{11} = 1 / h_{11}$ $y_{12} = -h_{12} / h_{11}$ $y_{21} = h_{21} / h_{11}$ $y_{22} = h_{22} - h_{12} h_{21} / h_{11}$		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td></tr><tr><td>I_a</td><td>y_{11}</td><td>$-y_{11}$</td><td>y_{12}</td><td>$-y_{12}$</td></tr><tr><td>I_b</td><td>$-y_{11}$</td><td>y_{11}</td><td>$-y_{12}$</td><td>y_{12}</td></tr><tr><td>I_c</td><td>y_{21}</td><td>$-y_{21}$</td><td>y_{22}</td><td>$-y_{22}$</td></tr><tr><td>I_d</td><td>$-y_{21}$</td><td>y_{21}</td><td>$-y_{22}$</td><td>y_{22}</td></tr></table>		V_a	V_b	V_c	V_d	I_a	y_{11}	$-y_{11}$	y_{12}	$-y_{12}$	I_b	$-y_{11}$	y_{11}	$-y_{12}$	y_{12}	I_c	y_{21}	$-y_{21}$	y_{22}	$-y_{22}$	I_d	$-y_{21}$	y_{21}	$-y_{22}$	y_{22}
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I_b	$-y_{11}$	y_{11}	$-y_{12}$	y_{12}																										
I_c	y_{21}	$-y_{21}$	y_{22}	$-y_{22}$																										
I_d	$-y_{21}$	y_{21}	$-y_{22}$	y_{22}																										
dvojbran popsaný parametry K	2-port_K		k₁₁, k₁₂, k₂₁, k₂₂ – sériově-paralelní hybridní parametry $\begin{bmatrix} I_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} k_{11} & k_{12} \\ k_{21} & k_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ I_2 \end{bmatrix}$ $y_{11} = k_{11} - k_{12} k_{21} / k_{22}$ $y_{12} = k_{12} / k_{22}$ $y_{21} = -k_{21} / k_{22}$ $y_{22} = 1 / k_{22}$		<table><tr><td></td><td>V_a</td><td>V_b</td><td>V_c</td><td>V_d</td></tr><tr><td>I_a</td><td>y_{11}</td><td>$-y_{11}$</td><td>y_{12}</td><td>$-y_{12}$</td></tr><tr><td>I_b</td><td>$-y_{11}$</td><td>y_{11}</td><td>$-y_{12}$</td><td>y_{12}</td></tr><tr><td>I_c</td><td>y_{21}</td><td>$-y_{21}$</td><td>y_{22}</td><td>$-y_{22}$</td></tr><tr><td>I_d</td><td>$-y_{21}$</td><td>y_{21}</td><td>$-y_{22}$</td><td>y_{22}</td></tr></table>		V_a	V_b	V_c	V_d	I_a	y_{11}	$-y_{11}$	y_{12}	$-y_{12}$	I_b	$-y_{11}$	y_{11}	$-y_{12}$	y_{12}	I_c	y_{21}	$-y_{21}$	y_{22}	$-y_{22}$	I_d	$-y_{21}$	y_{21}	$-y_{22}$	y_{22}
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I_b	$-y_{11}$	y_{11}	$-y_{12}$	y_{12}																										
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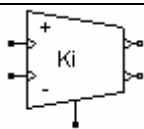
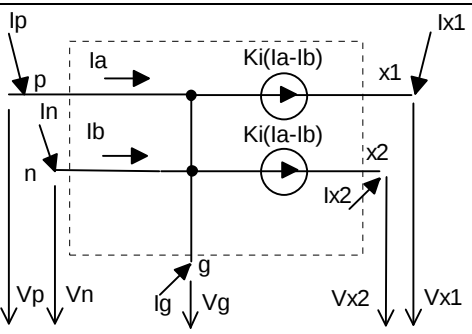
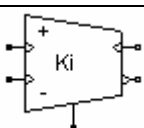
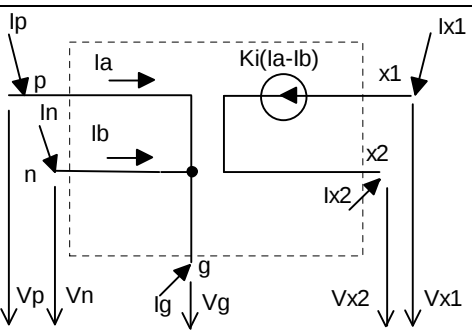
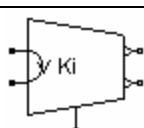
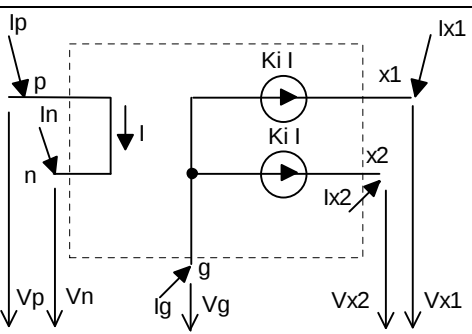
dvojbran popsaný parametry Y	2-port_Y		y11, y12, y21, y22 – admitanční parametry $\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} y_{11} & y_{12} \\ y_{21} & y_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>Vd</td></tr><tr><td>Ia</td><td>y11</td><td>-y11</td><td>y12</td><td>-y12</td></tr><tr><td>Ib</td><td>-y11</td><td>y11</td><td>-y12</td><td>y12</td></tr><tr><td>Ic</td><td>y21</td><td>-y21</td><td>y22</td><td>-y22</td></tr><tr><td>Id</td><td>-y21</td><td>y21</td><td>-y22</td><td>y22</td></tr></table>		Va	Vb	Vc	Vd	Ia	y11	-y11	y12	-y12	Ib	-y11	y11	-y12	y12	Ic	y21	-y21	y22	-y22	Id	-y21	y21	-y22	y22											
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Ic	y21	-y21	y22	-y22																																					
Id	-y21	y21	-y22	y22																																					
dvojbran popsaný parametry Z	2-port_Z		z11, z12, z21, z22 – impedanční parametry $\begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = \begin{bmatrix} z_{11} & z_{12} \\ z_{21} & z_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix}$		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>Vd</td><td>I2</td></tr><tr><td>Ia</td><td>1/z11</td><td>-1/z11</td><td></td><td></td><td>-z12/z11</td></tr><tr><td>Ib</td><td>-1/z11</td><td>1/z11</td><td></td><td></td><td>z12/z11</td></tr><tr><td>Ic</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>Id</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>z21/z11</td><td>-z21/z11</td><td>-1</td><td>1</td><td>z22 - z12z21/z11</td></tr></table>		Va	Vb	Vc	Vd	I2	Ia	1/z11	-1/z11			-z12/z11	Ib	-1/z11	1/z11			z12/z11	Ic					1	Id					-1		z21/z11	-z21/z11	-1	1	z22 - z12z21/z11
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Ib	-1/z11	1/z11			z12/z11																																				
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	z21/z11	-z21/z11	-1	1	z22 - z12z21/z11																																				
obecný impedanční konvertor	GIC		a11, a22 – kaskádní parametry $\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} a_{11} & 0 \\ 0 & a_{22} \end{bmatrix} \begin{bmatrix} V_2 \\ -I_2 \end{bmatrix}$		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>Vd</td><td>I2</td></tr><tr><td>Ia</td><td></td><td></td><td></td><td></td><td>-a22</td></tr><tr><td>Ib</td><td></td><td></td><td></td><td></td><td>a22</td></tr><tr><td>Ic</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>Id</td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td></td><td>-1</td><td>1</td><td>a11</td><td>-a11</td><td></td></tr></table>		Va	Vb	Vc	Vd	I2	Ia					-a22	Ib					a22	Ic					1	Id					-1		-1	1	a11	-a11	
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obecný impedanční invertor	GII		a12, a21 – kaskádní parametry $\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} 0 & a_{12} \\ a_{21} & 0 \end{bmatrix} \begin{bmatrix} V_2 \\ -I_2 \end{bmatrix}$		<table><tr><td></td><td>Va</td><td>Vb</td><td>Vc</td><td>Vd</td></tr><tr><td>Ia</td><td></td><td></td><td>a21</td><td>-a21</td></tr><tr><td>Ib</td><td></td><td></td><td>-a21</td><td>a21</td></tr><tr><td>Ic</td><td>-1/a12</td><td>1/a12</td><td></td><td></td></tr><tr><td>Id</td><td>1/a12</td><td>-1/a12</td><td></td><td></td></tr></table>		Va	Vb	Vc	Vd	Ia			a21	-a21	Ib			-a21	a21	Ic	-1/a12	1/a12			Id	1/a12	-1/a12													
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pozitivní proudový konvektor I. generace	CCI+		-		<table> <tr> <th></th><th>V_x</th><th>V_y</th><th>V_z</th><th>V_g</th><th>I</th></tr> <tr> <th>I_x</th><td></td><td></td><td></td><td></td><td>1</td></tr> <tr> <th>I_y</th><td></td><td></td><td></td><td></td><td>1</td></tr> <tr> <th>I_z</th><td></td><td></td><td></td><td></td><td>1</td></tr> <tr> <th>I_g</th><td></td><td></td><td></td><td></td><td>-3</td></tr> <tr> <td></td><td>1</td><td>-1</td><td></td><td></td><td></td></tr> </table>		V_x	V_y	V_z	V_g	I	I_x					1	I_y					1	I_z					1	I_g					-3		1	-1			
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negativní proudový konvektor III. generace	CCIII-		-		<table border="1"> <thead> <tr> <th></th><th>V_x</th><th>V_y</th><th>V_z</th><th>V_g</th><th>I</th></tr> </thead> <tbody> <tr> <td>I_x</td><td></td><td></td><td></td><td></td><td>1</td></tr> <tr> <td>I_y</td><td></td><td></td><td></td><td></td><td>-1</td></tr> <tr> <td>I_z</td><td></td><td></td><td></td><td></td><td>-1</td></tr> <tr> <td>I_g</td><td></td><td></td><td></td><td></td><td>1</td></tr> <tr> <td></td><td>1</td><td>-1</td><td></td><td></td><td></td></tr> </tbody> </table>		V_x	V_y	V_z	V_g	I	I_x					1	I_y					-1	I_z					-1	I_g					1		1	-1			
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obecný proudový konvektor (GCC – General Current Conveyor)	GCC		$a=V_x/V_y$ $b_1=I_y/I_x$ $b_2=I_z/I_x$ [a b1 b2] CCI+ [1 1 1] CCI- [1 1 -1] CCII+ [1 0 1] CCII- [1 0 -1] CCIII+ [1 -1 1] CCIII- [1 -1 -1]		<table><tr><th></th><th>V_x</th><th>V_y</th><th>V_z</th><th>V_g</th><th>I</th></tr><tr><td>I_x</td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_y</td><td></td><td></td><td></td><td></td><td>b_1</td></tr><tr><td>I_z</td><td></td><td></td><td></td><td></td><td>b_2</td></tr><tr><td>I_g</td><td></td><td></td><td></td><td>$-1-b_1-b_2$</td><td></td></tr><tr><td></td><td>1</td><td>$-a$</td><td></td><td></td><td></td></tr></table>		V_x	V_y	V_z	V_g	I	I_x					1	I_y					b_1	I_z					b_2	I_g				$-1-b_1-b_2$			1	$-a$																																																
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Prvek CTTA (Current Through Transconductance Amplifier)	CTTA 	b – zisk interního zdroje proudu; implicitní nastavení b = 1 gm - transkonduktance		<table><tr><th></th><th>Vp</th><th>Vn</th><th>Vz</th><th>Vx1</th><th>Vx2</th><th>Vg</th><th>I</th></tr><tr><td>Ip</td><td></td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>In</td><td></td><td></td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td>Iz</td><td></td><td></td><td></td><td></td><td></td><td></td><td>-b</td></tr><tr><td>Ix1</td><td></td><td></td><td>-gm</td><td></td><td>gm</td><td></td><td></td></tr><tr><td>Ix2</td><td></td><td></td><td>-gm</td><td></td><td>gm</td><td></td><td></td></tr><tr><td>Ig</td><td></td><td></td><td>2gm</td><td></td><td>-2gm</td><td></td><td>b</td></tr><tr><td></td><td>1</td><td>-1</td><td></td><td></td><td></td><td></td><td></td></tr></table>		Vp	Vn	Vz	Vx1	Vx2	Vg	I	Ip							1	In							-1	Iz							-b	Ix1			-gm		gm			Ix2			-gm		gm			Ig			2gm		-2gm		b		1	-1																						
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Proudový zesilovač CA_DICO (Current Amplifier_Differential Input Common Output)	CA_DICO		Ki – proudové zesílení		<table><tr><th></th><th>V_p</th><th>V_n</th><th>V_{x1}</th><th>V_{x2}</th><th>V_g</th><th>I_a</th><th>I_b</th></tr><tr><th>I_p</th><td></td><td></td><td></td><td></td><td></td><td>1</td><td></td></tr><tr><th>I_n</th><td></td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><th>I_{x1}</th><td></td><td></td><td></td><td></td><td></td><td>$-K_i$</td><td>K_i</td></tr><tr><th>I_{x2}</th><td></td><td></td><td></td><td></td><td></td><td>$-K_i$</td><td>K_i</td></tr><tr><th>I_g</th><td></td><td></td><td></td><td></td><td></td><td>$2K_i - 1$</td><td>$-2K_i - 1$</td></tr><tr><th></th><td>1</td><td></td><td></td><td></td><td>-1</td><td></td><td></td></tr><tr><th></th><td></td><td>1</td><td></td><td></td><td>-1</td><td></td><td></td></tr></table>		V_p	V_n	V_{x1}	V_{x2}	V_g	I_a	I_b	I_p						1		I_n							1	I_{x1}						$-K_i$	K_i	I_{x2}						$-K_i$	K_i	I_g						$2K_i - 1$	$-2K_i - 1$		1				-1					1			-1		
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Proudový rozbočovač CS_SIMO (Current Splitter_Single Input Multiple Output)	CS_SIMO		b1, b2, b3 – proudové přenosy ze vstupu do výstupů 1, 2, 3; implicitně nastaveny na hodnotu 1		<table><tr><td></td><td>V_p</td><td>V_{x1}</td><td>V_{x2}</td><td>V_{x3}</td><td>V_g</td><td>I</td></tr><tr><td>I_p</td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_{x1}</td><td></td><td></td><td></td><td></td><td></td><td>$-b_1$</td></tr><tr><td>I_{x2}</td><td></td><td></td><td></td><td></td><td></td><td>$-b_2$</td></tr><tr><td>I_{x3}</td><td></td><td></td><td></td><td></td><td></td><td>$-b_3$</td></tr><tr><td>I_g</td><td></td><td></td><td></td><td></td><td>$b_1 + b_2 + b_3 - 1$</td><td></td></tr><tr><td></td><td>1</td><td></td><td></td><td></td><td>-1</td><td></td></tr></table>		V_p	V_{x1}	V_{x2}	V_{x3}	V_g	I	I_p						1	I_{x1}						$-b_1$	I_{x2}						$-b_2$	I_{x3}						$-b_3$	I_g					$b_1 + b_2 + b_3 - 1$			1				-1																
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Proudový rozbočovač CS_DIMO (Current Splitter_Differential Input Multiple Output)	CS_DIMO		b1, b2, b3 – proudové přenosy ze vstupu do výstupů 1, 2, 3; implicitně nastaveny na hodnotu 1		<table><tr><td></td><td>V_p</td><td>V_n</td><td>V_{x1}</td><td>V_{x2}</td><td>V_{x3}</td><td>V_g</td><td>I</td></tr><tr><td>I_p</td><td></td><td></td><td></td><td></td><td></td><td></td><td>1</td></tr><tr><td>I_n</td><td></td><td></td><td></td><td></td><td></td><td></td><td>-1</td></tr><tr><td>I_{x1}</td><td></td><td></td><td></td><td></td><td></td><td></td><td>$-b_1$</td></tr><tr><td>I_{x2}</td><td></td><td></td><td></td><td></td><td></td><td></td><td>$-b_2$</td></tr><tr><td>I_{x3}</td><td></td><td></td><td></td><td></td><td></td><td></td><td>$-b_3$</td></tr><tr><td>I_g</td><td>1</td><td></td><td></td><td></td><td>-1</td><td>$b_1 + b_2 + b_3$</td><td></td></tr><tr><td></td><td>1</td><td>-1</td><td></td><td></td><td></td><td></td><td></td></tr></table>		V_p	V_n	V_{x1}	V_{x2}	V_{x3}	V_g	I	I_p							1	I_n							-1	I_{x1}							$-b_1$	I_{x2}							$-b_2$	I_{x3}							$-b_3$	I_g	1				-1	$b_1 + b_2 + b_3$			1	-1					
	V_p	V_n	V_{x1}	V_{x2}	V_{x3}	V_g	I																																																														
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I_g	1				-1	$b_1 + b_2 + b_3$																																																															
	1	-1																																																																			
neznámá součástka	Unknown		Tímto symbolem se označí všechny součástky, načtené ze vstupního souboru *.cir, které nejsou definovány v knihovně SNAP.LIB.																																																																		